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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Mariagiovanna Sami et al.  
Application No. : 09/976,241  
Filed : October 11, 2001  
For : PROCESSOR ARCHITECTURE

Art Unit : 2183  
Docket No. : 851763.411  
Date : February 15, 2002

Box Missing Parts  
Commissioner for Patents  
Washington, DC 20231

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents:

In accordance with 37 C.F.R. §§ 1.56 and 1.97 through 1.98, applicants wish to make known to the Patent and Trademark Office the references set forth on the attached form PTO-1449 (copies of the cited references are enclosed). As to any reference supplied, applicants do not admit that it is "prior art" under 35 U.S.C. §§ 102 or 103, and specifically reserve the right to traverse or antedate any such reference, as by a showing under 37 C.F.R. § 1.131 or other method. Although the aforesaid references are made known to the Patent and Trademark Office in compliance with applicants' duty to disclose all information they are aware of which is believed relevant to the examination of the above-identified application, applicants believe that their invention is patentable.

Please acknowledge receipt of this Information Disclosure Statement and kindly make the cited references of record in the above-identified application.

Respectfully submitted,

Mariagiovanna Sami et al.

Seed Intellectual Property Law Group PLLC



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Enclosures:

Postcard

Form PTO-1449

Cited References (4)

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FORM PTO-1449  
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PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.  
851763.411APPLICATION NO.  
09/976,241

## INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

APPLICANTS

Mariagiovanna Sami et al.

FILING DATE

October 11, 2001

GROUP ART UNIT

2183

## U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA	4,228,497	10/14/80	Gupta et al.	364	200	
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION	
					YES	NO
	AK	0 569 312 A2	11/10/93	EP		
	AL					
	AM					
	AN					
	AO					

## OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	AP	Lozano, L. et al., "Exploiting Short-Lived Variables in Superscalar Processors", pp. 292-302, XP000585373, November 29, 1995.
	AQ	Liebchen, A. et al., "Dynamic Reordering of High Latency Transactions Using a Modified Micropipeline", pp. 336-340, XP000344638, October 11, 1992.
	AR	

EXAMINER

DATE CONSIDERED

\* EXAMINER: Initial if reference considered, whether or not criteria is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).